

REMARKS

Applicant thanks Examiner for the detailed review of the application.

Claim Objections

The Office Action has objected to claim 11 based on and being listed twice. This typographical error has been corrected in amended claim 11. Similarly, the comma and whitespace at the end of claim 15 has been removed.

Claim Rejections -35 USC § 101

Please note applicant has amended paragraph 0022 to remove reference to implementation of one or more elements in hardware, software, a propagated signal, or a combination thereof. Furthermore, please note that the removed reference was to one or more elements of the various embodiments; however, applicant's independent claims, such as claim 1, explicitly includes a communication or protocol stack., which is described throughout applicant's description. Specifically, a transaction layer is often described to assemble packets, through use of hardware or firmware, i.e. hardware with embedded software, all of which includes tangible and/or functional media. In fact, the transaction layer is to specifically produce a tangible packet header including the claimed fields, which may be physically transmitted over an interconnect.

Conclusion

Therefore, applicant respectfully submits that independent claims 1, 6, 11, and 39, as well as their dependent claims, are now in condition for allowance for at least the reasons stated above. If there are any additional charges, please charge Deposit Account No. 50-0221. If a telephone interview would in any way expedite the prosecution of the present application, the Examiner is invited to contact David P. McAbee at (503) 712-4988.

Respectfully submitted,
Intel Corporation

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